

M.L. SYED ALI. B.E, M.E

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OBJECTIVE:

Seeking a position in an organization that offers avenue to exhibit and render all my skills and talents. Also keen to perform effectively the assigned job with full responsibilities in growth of the department and organization.

SUMMARY:

- Having 15+ Years of Teaching Experience including 11 Years after Post Graduate
- Flexible and Technically Excellent Lecturer.
- Pragmatic, skillful and technically handed Engineer.

WORK EXPERIENCE:

ACADEMICS:

Total Experience: 16+ Years

College	Position	Dept	Duration		Years of Experience
			From	To	
Mohamed Sathak A.J College of Engineering, Egattur, Chennai	Assistant Professor	Electronics and Communication. Engineering	Jan 2007	Till to date	16 Years

INDUSTRY:*Experience: 08 Months*

Company	Position	Dept	Duration		Years of Experience
			From	To	
Norico Pvt Ltd, Goa	Service Engineer.	Service & Maintenance	Feb 2005	Sept 2005	8 months

EDUCATIONAL QUALIFICATION:

Examination Passed	Board/ University	Institution	Year of Passing	Marks	Class
M.E(Applied Electronics)	Anna university chennai	C.Abdul hakeem college of Engg,Vellore,INDIA	2011	72%	First
B.E (Electronics & Communication Engg)	Manonmaniam Sundaranar University	Government College of Engineering, Tirunelveli, INDIA	2004	69%	First
H.SC	State board (Tamil Nadu)	Masood Thaika Higher Secondary School,Kadayanallur, INDIA	2000	90%	First
S.S.L.C	State board (Tamil Nadu)	I.C.I.Government Higher Secondary School,Tenkasi, INDIA	1998	89%	First

PROFESSIONAL ASSOCIATIONS MEMBERSHIP:

1. IRED (Membership ID: AM101000583152)
2. IAENG (Membership ID: 222477.)

SUBJECTS HANDLED:

- Linear Integrated Circuits
- RF and Microwave Engineering
- Microprocessors and microcontrollers
- Digital signal processing
- Circuit Theory
- Digital electronics.

LIST OF LABS HANDLED :

- **Projects Design Lab**
- Linear Integrated Circuits Lab
- Optical and Microwave Lab.
- Microprocessors and microcontrollers Lab.
- Electronic devices Lab
- Digital signal processing Lab
- Digital electronics Lab

ACADEMIC ACHIEVEMENTS:

- Completed an online certified course on “ **Linear Circuits –DC Analysis** ” from **Georgia Institute Of Technology ,USA**
- Completed an online certified course on “ **The Introduction Of IOT and Embedded Systems**” from **University Of California , USA**
- Completed an online certified course on “ **The Introduction Of Electronics** ” from **Georgia Institute Of Technology ,USA.**

Course/FDP/Webinar/ Workshop/Quiz Attended:

Online Technical Quiz in Electrical Engineering	Quiz 1 day	27-05-2020
Electric Vehicle	FDP 3 days	20-05-2020 to 22-05-2020
Online course on MATLAB Onramp	Online Course 1 day	19-04-2020
Two Days Webinar on 3D EM Simulation Involving CST Software	Webinar 2 days	18-05-2020 to 19-05-2020
A National level THREE days FDP on Embedded & IOT bootcamp	FDP 3 days	27-05-2020 to 29-05-2020
Screw ball Online Quiz	Quiz 1 day	30-5-2020
Online Quiz on “Applied Mathematics”	Quiz 1 day	27-05-2020
"Latest Trends and Advances in Ultra Wide Band Antenna Design"	WORKSHOP - 2 DAYS	10-11-2017 & 11.11.2017
" Introduction to Robotics" E YANTRA	WORKSHOP - 2 DAYS	15.11.2019 & 16.11.2019
"Intellectual Property Rights"	WORKSHOP - 1 DAY	13.12.2019
Introduction to the Internet of Things and Embedded Systems	4 Week Online course - Coursera	24.10.2018(completed)
Introduction to Electronics	4 Week Online course - Coursera	06.02.2019(completed)
Linear Circuits 1 : DC Analysis	4 Week Online course - Coursera	22.03.2019(completed)
Future of AI inspired by Customer Use Cases for AI-ML	webinar	9-4-2020
Robotic Process Automation: Basics and Beyond	webinar	10-4-2020
Introduction to VMWare Cloud	webinar	11-4-2020
Introduction to IOT	CISCO	20 hrs

PUBLICATIONS :

- Presented and Published a Paper in the Title of “ **High performance designs for linear algebra operations on FPGA**” in the proceeding of **National Conference on EMERGING IT’S** in one day National Conference Conducted by Department of Information Technology, National College of Engineering, Tirunelveli on March 20, 2010.

- Presented a Paper in the Title of “High performance designs for linear algebra operations on Reconfigurable hardware ” in a National Conference on Web technology and open source in one day National Conference Conducted by Dept of Computer science, Jaya Engineering college Tiruninravur, on March 17,2011.
- Presented a Paper in the Title of “HARD EXUDATE DETECTION IN RETINAL IMAGES USING STATISTICAL PARAMETERS” in a World Journal of Engineering research and Technology ,Vol 3,Issue ,Page no 274-283.

Achievements for the Department and the College:

- Placement Coordinator with placement record of 100%
- QA Committee Member.
- Acted as a **Judge** in Paper presentation contest in various National and State level Symposium.
- Framed and executed technical events like **Circuit Design and Debugging Contest** for the State level Technical Programme. Also motivated and guided the students in accruing sponsors in organization of the programs.
- Participated in **Faculty development program** on INSTRUCTIONAL DESIGN AND DELIVERY conducted by **National Technical Teachers Training Institute& Research** on 19th, 20th, and 21st March 2009 at Mohamed Sathak A.J.College of Engineering, Chennai.
- IETE Students forum Department co ordinator

TECHNICAL EXPERTISE:

Languages: C and C++.

Tools Used: MS- Office, PSPICE,MATLAB, XILINX and MODELSIM

LANGUAGES KNOWN:

English, Tamil and Urdu.

PERSONAL HISTORY :

Father's name : Mr.M.K.Liyakath Ali
Date of birth : 11-06-1983
Gender : Male
Nationality : Indian
Marital Status : Married
Passport Number : P2318298.
Expiry : March 2027
Permanent Address : 126 C5, Sindha Mathar Pallivasal Street,
Kadayanallur-627751, Tirunelveli (Dist),
TamilNadu,
INDIA.

DECLARATION:

I hereby declare that all above information are true and complete to the best of my knowledge and belief. If given an opportunity, would like to assure most Satisfactory service with best performance.
