

**MOHAMED SATHAK A J COLLEGE OF ENGINEERING, CHENNAI - 603103**

**(An Autonomous Institution)**

**Timetable B.E./B.Tech. Degree Examinations - Nov-'25 / Dec-'25**

**Regulations - 2024**

**Choice Based Credit Systems (CBCS)**

**Branch Name: 204 B.Tech – Electronics Engineering w/s in VLSI Design and Technology**

| S.No | Sem | Sub.Code | Subject Name                                     | Date       | Session |
|------|-----|----------|--------------------------------------------------|------------|---------|
| 1    | 1   | 24MA101  | Matrices and Calculus                            | 01-12-2025 | FN      |
| 2    | 1   | 24PY111  | Engineering Physics**                            | 02-12-2025 | FN      |
| 3    | 1   | 24CH111  | Engineering Chemistry**                          | 02-12-2025 | AN      |
| 4    | 1   | 24EN101  | Technical Communication - I                      | 03-12-2025 | AN      |
| 5    | 1   | 24TA101  | Heritage of Tamils / தமிழ் மரபு                  | 09-12-2025 | FN      |
| 6    | 2   | 24MA202  | Transforms and Fourier Analysis                  | 03-12-2025 | FN      |
| 7    | 2   | 24EN201  | Technical Communication - II                     | 04-12-2025 | AN      |
| 8    | 2   | 24TA201  | தமிழரும் தொழில்நுட்பமும் / Tamils and Technology | 04-12-2025 | FN      |
| 9    | 2   | 24EE201  | Electric Circuit Analysis                        | 05-12-2025 | FN      |
| 10   | 2   | 24VL201  | Theory of Semiconductor Devices                  | 06-12-2025 | FN      |
| 11   | 2   | 24GE101  | Basic Civil and Mechanical Engineering           | 11-12-2025 | FN      |
| 12   | 2   | 24CS111  | Programming In C**                               | 22-12-2025 | FN      |
| 13   | 3   | 24EE311  | Principles of Electrical Engineering**           | 08-12-2025 | FN      |
| 14   | 3   | 24MA302  | Linear Algebra and Numerical Method              | 10-12-2025 | FN      |
| 15   | 3   | 24EC301  | Signals and Systems                              | 12-12-2025 | FN      |
| 16   | 3   | 24EC312  | Digital System Design**                          | 15-12-2025 | FN      |
| 17   | 3   | 24EC311  | Analog Circuits - I**                            | 17-12-2025 | FN      |

FN: FORENOON (10.00 A.M. TO 01. 00 P.M.)

AN:AFTERNOON (02.00 P.M. TO 5.00 P.M.)

  
DCoE 12/11/25

  
Controller of Examinations



  
Principal 12/11/25